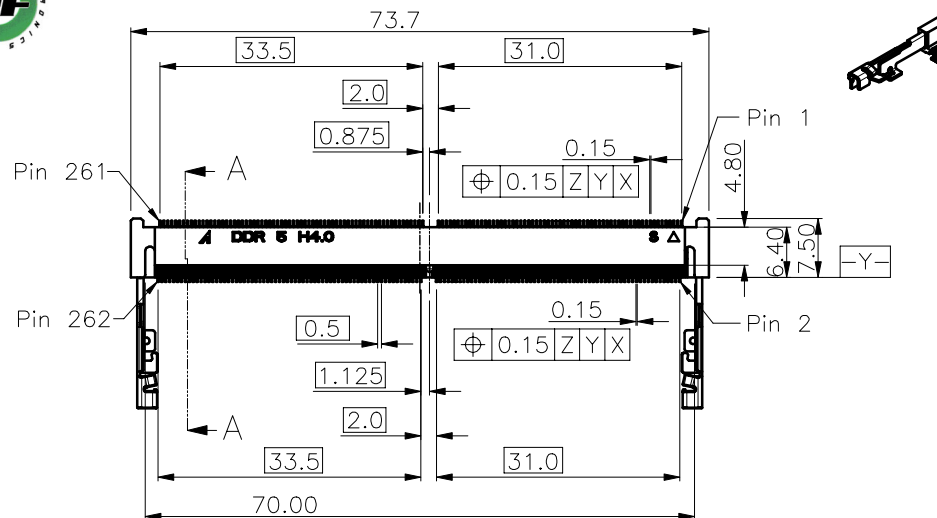
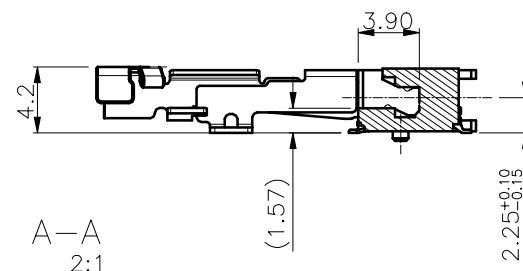
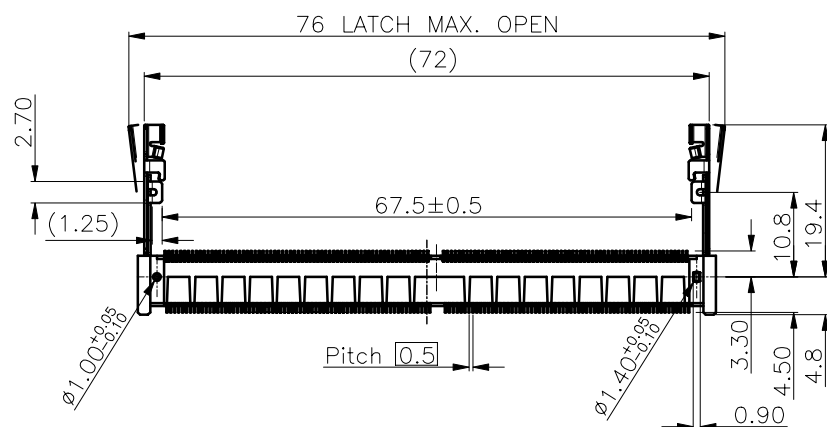
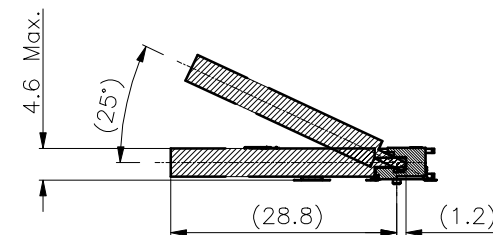
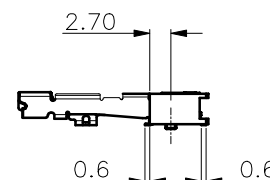
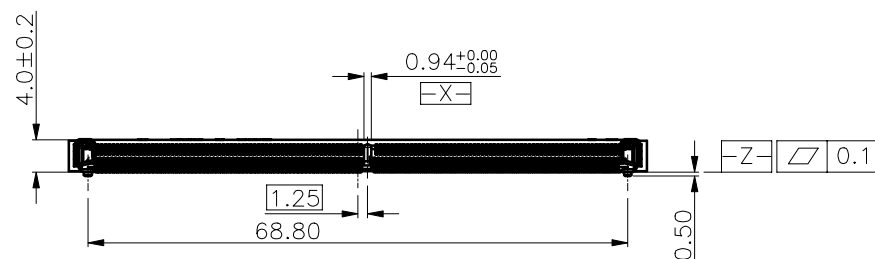
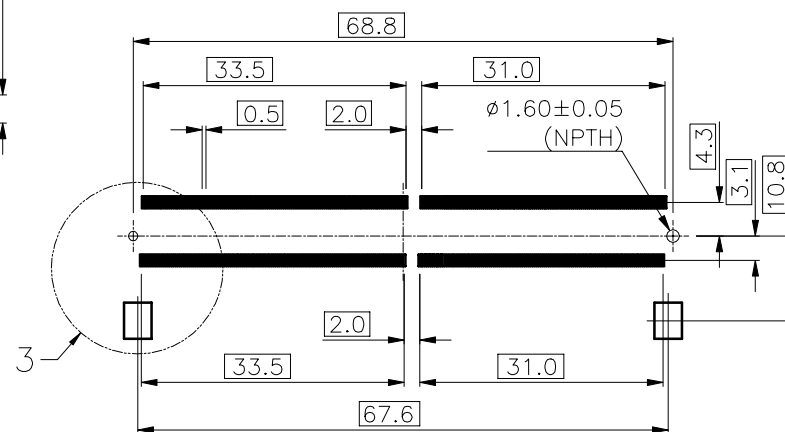
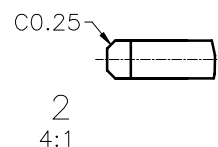
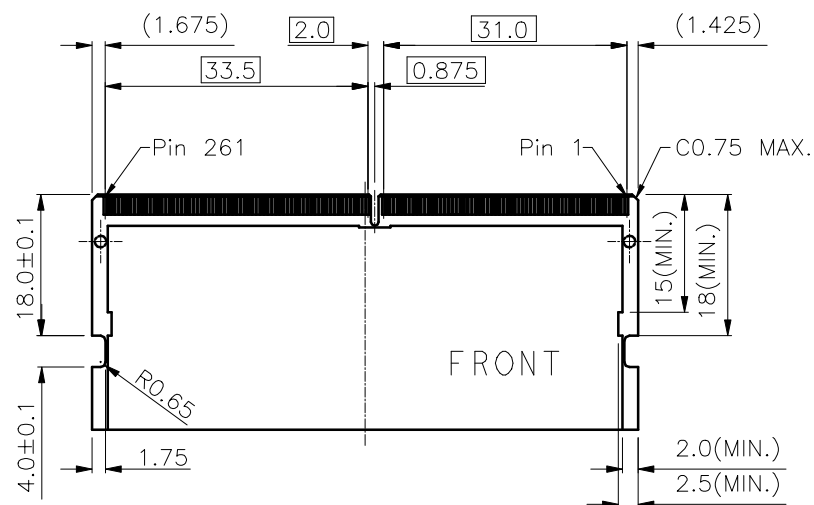
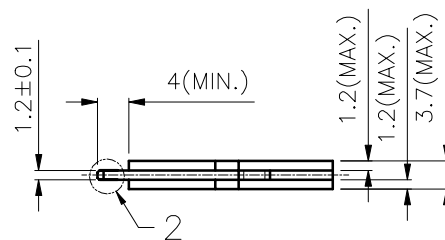
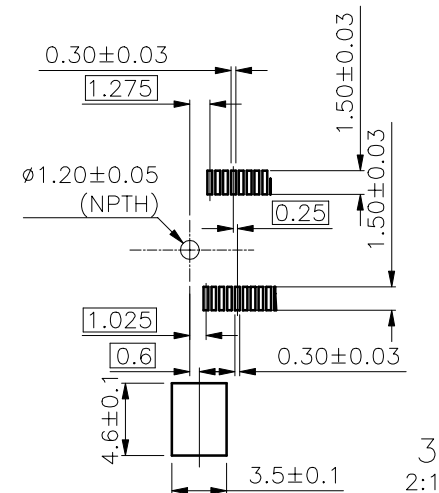
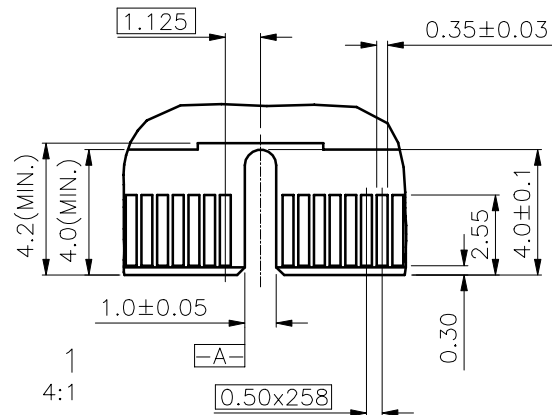
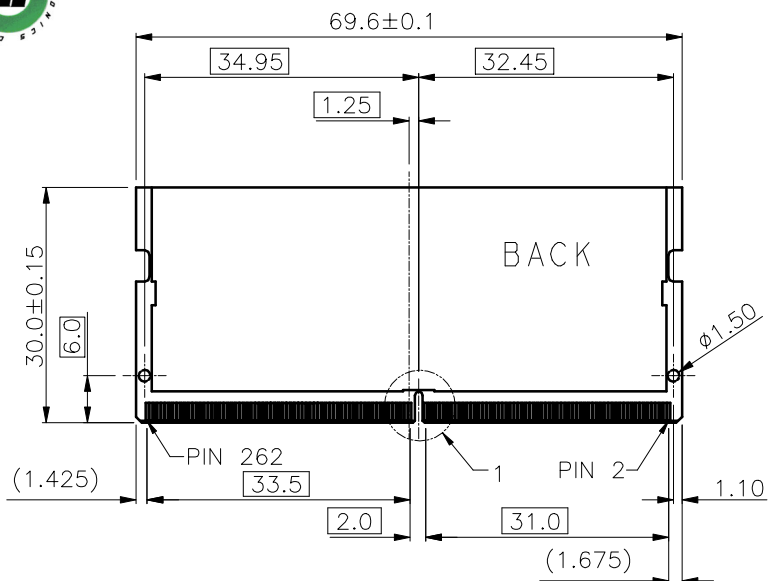


1.GOLD 1u" ON CONTACT



QUALITY SYMBOLS MAJOR  CRITICAL 	DRAWN BY CHEN CHUN YUAN 23'/08/30			
	CHECKED BY _____ DATE _____			
GENERAL TOLERANCES (UNLESS SPECIFIED) X. ± 0.5 .X ± 0.25 .XX ± 0.15 .XXX ± 0.1 ANGLES $\pm 2^\circ$	TSO I CHIAO 23'/08/30 APPROVED BY HUANG KUO HUA 23'/08/30		TITLE DDR 5 CONN. 262 Pin H 4.0 STD Type	
	UNITS mm		SIZE A4	RFQ NO. N/A
	SCALE 1:1	SHEET NO. 1 OF 2	REV D	DWG NO. 52746-xxxxx-xxx



RECOMMEND PCB LAYOUT(STANDARD)
THE TOLERANCE UNLESS SPECIFIED IS $\pm 0.05\text{MM}$

QUALITY SYMBOLS MAJOR CRITICAL GENERAL TOLERANCES (UNLESS SPECIFIED) X. ± 0.5 .X ± 0.25 .XX ± 0.15 .XXX ± 0.1 ANGLES $\pm 2^\circ$	DRAWN BY CHEN CHUN YUAN 23'/08/30		DATE 23'/08/30	
	CHECKED BY TSOI CHIAO 23'/08/30		DATE 23'/08/30	
	APPROVED BY HUANG KUO HUA 23'/08/30		DATE 23'/08/30	
	UNITS mm		SIZE A4	RFQ NO. N/A
SCALE 1:1	SHEET NO. 2 OF 2	REV D	DWG NO. 52746-xxxxx-xxx	